

Pune Vidyarthi Griha's
COLLEGE OF ENGINEERING & TECHNOLOGY Pune-411009

Time Table With effect from:20/08/2018

Class:BE [E & TC-A]

Academic Year : 2018-2019

Term:I

| TIME | MONDAY | TUESDAY | WEDNESDAY | THURSDAY | FRIDAY | SATURDAY |
|--------------|--------------------------------------|---|--|---|--|--------------|
| 8.00 -9.00 | | | | | | |
| 9.00 - 10.00 | VLSI DESIGN 107-[VUG] | | VLSI DESIGN 107-[VUG] | | VLSI DESIGN 107-[VUG] | AUDIT COURSE |
| 10.00-11.00 | CNS 107-[SRL] | RMT 107-[TSK] | CNS 107-[SRL] | RMT 107-[TSK] | RMT 107-[TSK] | AUDIT COURSE |
| 11.00 -12.00 | | | | | | |
| 12.00 -01.00 | AI \ EPD 405 \ 107 [MPA]\[NSN] | AI \ EPD 405 \ 107 [MPA]\[NSN] | DIVP\IOT 107 \ 405 [KJK] \ [CBJ] | DIVP\IOT 107 \ 405 [KJK] \ [CBJ] | AI \ EPD 405 \ 107 [MPA]\[NSN] | |
| 01.00-02.00 | PROJECT | B1-VLSI[VUG] B2-RMT[TSK] B3-CNS[SRL] B4-ELECTIVE I[IOT][CBJ] | B1-ELECTIVE -I[IOT][CBJ] B2-VLSI[PSD] B3-RMT[TSK] B4-CNS[MHB] | B1-CNS[MHB] B2-ELECTIVE-I[IOT][CBJ] B3-VLSI[PSD] B4-RMT[TSK] | B1-RMT[TSK] B2-CNS[SRL] B3-ELECTIVE- I[DIVP][KJK] B4-VLSI[PSD] | |
| 02.00-3.00 | | | | | | |
| 3.00- 4.00 | | DIVP\IOT 107\ 405 [KJK] \ [CBJ] | T & P ACTIVITY | CNS 107-[SRL] | T & P ACTIVITY | |
| 4.00 -5.00 | | | | CNS 107-[SRL] | | |
| 5.00 -6.00 | | | | | | |

Prof. M. P. Potadar
Time Table Incharge

Prof. O.S.Pawaskar
Time Table Coordinator

Dr. K. J.Kulkarni
Head-E & TC

Dr. Y. P. Nerkar
Principal