Name:

Prof. Mrs. Priya Sandeep Deshpande

Designation:

Assistant Professor

Contact Details:

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Educational Qualifications:

- M.E. (E & TC, Signal Processing), Pune University, Pune, May 2011.
- B.E. (Electronics), Shivaji University, Kolhapur, May 2003
- HSC, New College Kolhapur, 1999

Experience:

Teaching: 14 Years

Research Interests:

- VLSI Design & Technology
- AI/ML/IoT

Number of Publications:

International Conference: 01

National Conference:02

Professional Society Memberships:

• ISTE Membership Number:LM378



Number of Workshops organized:02

Number of Conferences / Seminars organized:10

Number of FDPs / STTPs attended:15

Number of Workshops attended:12

Number of Conferences / Seminars attended: 15

Number of Online Certification Courses done:03

Number of Webinar attended:20

Number of times served as a Resource Person:02

Number of times served as a Reviewer / Judge:02

Details of Research papers published:

- "Review Of AES Implementation strategies on FPGA"
 National Conference on "Futuristic Applications in Electronics Engineering" held at AISSMS, Pune, 10th and 11th March 2011.
- "AES Encryptor Implementation On FPGA"
 National Conference on "Information and Communication technology" held at RSCOE, Pune, 26th and 27th May 2011
- "High throughput Sub Pipelined Implementation Of AES Algorithm."
 2011 IEEE International Conference in Communication and Information technology, Kanyakumari, 11-15 December 2011

Details of FDPs / STTPs attended in last 5 years:

- Attended online workshop on Universal Human Value on the theme "Inculcating Universal Human Values in Technical Education" during 12-16 October, 2020 asorganized by All India Council for Technical Education(AICTE).
- Attended Faculty Orientation Workshop on SE (E&TC/Elex) Revised Syllabus 2019C organized by Imperail college of Engineering, Wagholi from 22/06/2020 to 26/06/2020
- Completed the Bodhitree and SAFE Tools For Effective Online Teaching:A Hands on Workshop through BodhiTree, IIT Bombay funded by MHRD. Govt. of India on 21/06/2020

- Attended FDP on Linux by Dept of E &TC, PVG's COET, from 25th May to 29th May 2020
- Attended FDP on Scilab organised by Dept of E &TC, PVG's COET, from 18th May to 22nd May 2020
- Attended MHRD Innovation Cell IIC sessions on Entrepreneurship and Startups(Total 17 Sessions) in May 2020.
- Organized 2 days workshop for SE E & Tc students on "Python for data Sciences" in collaboration with 'Al Adventures ' from 3rd March 2020 to 4th March 2020.
- Participated in One day Workshop on Moodle Learning Management System at PVG'sCOET on 15th March 2019
- Participated in one day MATLAB hands-on session by MATLAB: Simulink and Antenna Toolbox at PVG'sCOET on 30th January 2019.
- Attended 2 days Faculty Development Program on Broadband Communication systems (2015 Revised Syllabus)at PCCOE, Ravet in December 2018.
- 2 week ISTE workshop on CMOS Mixed signal Radio Frequency design organised by IIT Kharagpur at Cummins College Of Engineering for Women Pune from 30th January 2017 to 4th February 2017
- Attended FDP on Broadband Communication systems at SKNCOE, Pune in December 2016
- A Two Day workshop on MATLAB by. Mr. Pratap Sanap, Persistant, Pune was organised along with Prof. Mrs. M. P. Atre on 16th July 2016 and 23rd July 2016.
- Got the Certificate of Learning in Inkspace Training organized at PVGCOET by Prof.Mrs.T.S.Khatavkar on 1st July 2016.
- Attended Modelika Workshop in June 2016

Major Portfolios Handled: (college level and department level)

- Assistant students Development officer
- Training and Placement Coordinator
- Student Internships coordinator
- Virtual Lab Coordinator
- Telecommunication Engineering Students Association Coordinator
- Kirloskar Vasundhara Coordinator

- Girls Empowerment and Development Coordinator
- Online Exam Coordinator