

Name:

Vaishali Umesh Gongane

**Designation:**

Assistant Professor

Contact Details:

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Educational Qualifications:

- M.E. (Electronics) (Digital System), Pune University, Pune, March 2011.
- B.E. (Electronics), Shivaji University, Kolhapur 2001

Experience:

Teaching: 12 Years

Research Interests:

- Embedded Systems
- VLSI Design

Number of Publications:

National Conference: 4

Recognition/ Awards:

Second prize in the Idea Competition, during the AICTE Sponsored One- Week Online Short Term Training Program on 'Advances in Biomedical Signal Processing with Hands-on'.

Professional Society Memberships:

- ISTE

Number of FDPs / STTPs organized: 02

Number of Workshops organized:01

Number of Conferences / Seminars organized:01

Number of FDPs / STTPs attended:6

Number of Workshops attended: 10

Number of Conferences / Seminars attended:02

Number of Online Certification Courses done: 10

Number of Webinar attended:02

Number of times served as a Resource Person:02

Number of times served as a Reviewer / Judge:02

Details of FDPs / STTPs attended in last 5 years:

- FDP on Electromagnetic Field Theory T.E (E&TC) Revised course 2019 from 22nd July 2021 to 25th July 2021.
- FDP on Skill Development T.E (E&TC) Revised course 2019 on 30th July 2021 and 31st July 2021.
- AICTE approved Online FDP on Data Science from 1/2/2021 to 5/2/2021.
- AICTE approved Online FDP on “Mathematical Foundations of Data Sciences and its Applications” 19th Jan 2021 -23rd Jan 2021
- FDP on Linux by Dept of E &TC, PVG’s COET, from 25th May to 29th May 2020.
- FDP on Scilab organised by Dept of E &TC, PVG’s COET, from 18th May to 22nd May 2020
- IIT-Spoken Tutorial FDP on eSim organised by AISSMS IOIT from 5th May to 9th May 2020, test was scheduled on 10th May 2020.
- **IIT -Spoken Tutorial FDP on Python** organised by RMD Sinhgad School of Engineering, Pune from 20th April to 24th April 2020, test was scheduled on 25th April 2020.
- AICTE Sponsored One- Week Online STTP on ‘Advances in Biomedical Signal Processing with Hands-on’, from 7th September to 12th September 2020.

- FDP on “ VLSI Design and Technology B.E (E&TC) Revised course 2015” from 18th July 2018 at PCCOE, Pune.
- FDP on “ Advanced Processors T.E (E&TC) Revised course 2015” from 14th to 16th December 2017 at MMCOE, Pune

Major Portfolios Handled: (college level and department level)

- Department BE Project coordinator.
- Department e-Yantra Coordinator.

Role in University Bodies

SPPU Paper Setter for BE Electrical subjects: Electromagnetics and VLSI Design