

Name:

Mrs.Rupali A. Girme

Designation:

Assistant Professor

**Contact Details:**

Email id: rag_fe@pvgcoet.ac.in

Educational Qualifications:

- Phd, Pursuing, SIT, Pune
- M.E. (VLSI & Embedded Systems), SKNCOE, Pune
- BE. (E & TC), SKNCOE, Pune

Experience:

- Industrial: 2 Years
- Teaching: 15 Years

Research Interests:

- Image processing
- Healthcare
- Artificial Intelligence

Research Projects:

- AI in Healthcare (Phd pursuing)

Number of Publications:

International Conference: 3

National Conference: 2

Number of Conferences / Seminars organized: 01

FDP on International Symposium on “Niche areas of Sustainable Development”,
(2-6) Sept'2024

Number of Workshops attended: 02

Number of Conferences / Seminars attended: 02

Number of Online Certification Courses done: 02

Details of Research papers published:

- Rupali S. Gadekar, Prof. Jagdale R. H, *“Review of the emerging Image fusion Techniques”*, International Interdisciplinary Conference on Changes, Challenges and Consequences in Commerce, Management, Engineering, Technology and Social Science, September, 2012.
- Rupali S. Gadekar, Prof. Jagdale R. H., *“FPGA Based Implementation of Image Fusion Technique”*, ePGCON, April 2013.
- Participated in ePGCON 2012, conducted on 23& 24 April at Cummins College of Engineering, Pune.
- Rupali S. Gadekar, Prof. Jagdale R. H, *“FPGA based design of Image fusion technique using DWT”*, International Conference on Advances in Engineering and Technology (ICAET), May 2013.
- Supriya Patil, Gadekar R. S. *“A paper on Implementation of Hybrid Channel allocation for Wireless Cellular Network”*, in International Conference on advance in Engg. & Technology. (ICAET-2013 SKN Lonavala), Pune.

Details of FDPs / STTPs attended in last 5 years:

- Attended STTP on *“DSP implementation On FPGA.”* At SKNCOE.
- Attended FDP on *“Research paper Writing: Tools and Techniques”*, at PVG’s COET.
- Attended FDP on *“AI for Sustainable Development”*, from 5 to 9 August, VIIT Pune.
- Attended Workshop on *“Teaching methodologies for Basic Electronics Engineering syllabus”*, 2012 Course & 2019 course.
- Attended One Week FDP on *“Basic Electronics Engineering”* (NEP 2020 in 2024 pattern) in KJCOEMR.
- Attended *“Outcome Based Education Philosophy for Curriculum Design”*, from 22 April to 26 April 2025 at PVG’s COET.
- Attended One Week FDP on *“Research Methodology, Ethics and Publication Tools”* at PVG’s COET.

Major Portfolios Handled: (college level and department level)

- NAAC Criteria 1 Coordinator’ 2024

- E & TC NBA co-ordinator
- BXE Lab Co-ordinator.
- Cultural coordinator etc.
- Parents Meet co-ordinator, Class-Teacher
- Co-ordinator of SIP Related activities in 2019 to 2024
- Co-ordinator of NAAC Cultural Event' 2024
- External Senior Supervisor in Bharti Vidyapeth College of Engineering for Women, 2023
- Co-ordinator of FDP on International Symposium on Niche areas of Sustainable Development, (2-6)Sept' 2024.

Any other information which is not covered in above points:

Received best paper for presenting paper on "*FPGA based design of Image fusion technique using DWT*", International Conference on Advances in Engineering and Technology (ICAET), May 2013, Lonavala.