



**Pune Vidyarthi Griha's  
COLLEGE OF ENGINEERING, TECHNOLOGY AND  
MANAGEMENT, PUNE-09**



Record No.: PVGCOET/MA/CAD/R/6

Revision: 00

Date: 1/1/2026

**Class Time Table**

Department: AI & DS  
W.E.F: 5/01/2026

Semester: II

Academic Year: 2025 - 2026  
Date: 29/12/2025

Name of the Class Teacher: Prof. Mrs. S. S. Shingade.

Day	9:00-10:00	10:00-11:00	11:00-12:00	12:00-1:00	1:00-2:00	2:00-3:00	3:00-4:00	4:00-5:00
Monday	DC PCB (Room Number 110)	BDA RSA (Room Number 110)	DC RSA (Room Number 110)	BDA RSA (Room Number 110)	CI SSS (Room Number 109)	BI RHIM (Room Number 109)	BI -CL-III-SSS (Room Number 310)	B2 -CL-IV-PCB (Room Number 311)
							B3 -CL-IV-RHIM (Room Number 309)	
Tuesday	DC PCB (Room Number 110)	BDA RSA (Room Number 110)	DC RSA (Room Number 110)	BDA RSA (Room Number 110)	BI RHIM (Room Number 109)	CI SSS (Room Number 109)	BI -CL-IV-PCB (Room Number 311)	B2 -CL-III-SSS (Room Number 310)
							B3 -CL-III-SSS (Room Number 309)	
Wednesday	BDA RSA (Room Number 110)	DC PCB (Room Number 110)	DC RSA (Room Number 110)	BDA RSA (Room Number 110)	BI RHIM (Room Number 109)	CI SSS (Room Number 109)	T & P	
Thursday	P R O J E C T			B R E A K	S T A G E - II			
Friday	P R O J E C T					S T A G E - II		
Saturday								Mentor Session

Abbreviation	Name of Course	Abbreviation	Name of Faculty
CI	Computational Intelligence	SSS	Prof. Mrs. S. S. Shingade
DC	Distributed Computing	PCB	Prof. Mrs. P. G. Bangale
BI	Business Intelligence	RHIM	Prof. Mr. R. H. Mule
BDA	Big Data Analysis	RSA	Dr. Mr. R. S. Apatre
CL-III	Computer Laboratory III	PCB + SSS	Prof. Mrs. P. G. Bangale + Prof. Mrs. S. S. Shingade
CL-IV	Computer Laboratory IV	RSA // RHIM	Dr. Mr. R. S. Apatre + Prof. Mr. R. H. Mule
T & P	Training & Placement		

*Shingade*  
Time Table Coordinator

*Shingade*  
Academic Coordinator

*Shingade*  
J.C.O.D.

*Shingade*  
In-charge

